

## EAST Search History

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	55606	FPGA or (Field program\$ gate array) or PLD or (program\$ logic device)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT	ADJ	ON	2007/01/23 11:33
L2	227077	crc or checksum or EDC or (error detect\$3) or parity	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT	ADJ	ON	2007/01/23 11:33
L3	12	((configur\$5 or reconfigur\$5) near8 l1) with (register or latch or buffer) with l2	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT	ADJ	ON	2007/01/23 11:42
L4	55	((configur\$5 or reconfigur\$5) with l1) same (register or latch or buffer) same l2	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT	ADJ	ON	2007/01/23 11:56
L5	4	((((configur\$5 or reconfigur\$5) with l1) and (register or latch or buffer) and l2 and counter).clm.	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT	ADJ	ON	2007/01/23 11:57
L6	4	(((((configur\$5 or reconfigur\$5) same l1) and (register or latch or buffer) and l2 and counter).clm.	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT	ADJ	ON	2007/01/23 11:57